

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

STRUCTURE AND METHOD OF PATTERNING A MAGNETIC
TUNNEL JUNCTION STACK FOR A MAGNETO-RESISTIVE
RANDOM ACCESS MEMORY

Application Number : 10/209 999

Confirmation Number:

First Named Applicant: Michael Gaidis

Attorney Docket Number: FIS920040017US1














Art Unit: 2818

Examiner: D. LE

Search string: (4619872 or 6365419 or 6495275 or 6548849 or 20020009616 or 20020048127 or 20030181056).pn








US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4619872	1986-10-28	Goor, et al.			
	2	6365419	2002-04-02	Durlam, et al.			
	3	6495275	2002-12-17	Kamiguchi, et al.			
	4	6548849	2003-04-15	Pan, et al.			

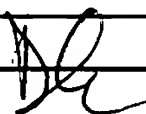
US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20020009616	2002-01-24	Kamiguchi, et al.			
	2	20020048127	2002-04-25	Fukuzawa, et al.			
	3	20030181056	2003-09-25	Kumar, et al.			

Signature

Examiner Name



Date

9/2005